VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA

Date: 03/11/2026

POSTPONEMENT OF SEMESTER-END EXAMINATIONS

The Semester-end Examinations of B.Tech., VI-Semester (VR20) and I-Semester (VR23) Supplementary Examinations for the following subjects have been rescheduled as given below due to 'Murasu' Cyclone.

Subject Code & Name	Actual Schedule of Examination Date & Time	Postponement of Examination Date & Time
20CE6404B-Foundation Engineering 20CE6404D-Railway & Tunnel Engg 20EC6404A-Cellular & Mobile Communications 20EC6404C-Introduction to Machine Learning 20EE6404A-Utilization of Electrical Energy 20EE6404B-Programmable Logic Controller 20IT6404B-Big Data 20IT6404C-Internet of Things 20ME6404C-Advanced Mechanics of Materials	03/11/2026 (FN) Tuesday 9.00 AM to 12.00 Noon	10/11/2026 (FN) Tuesday 9.00 AM to 12.00 Noon
23ES1104-Introduction to Programming	03/11/2026 (AN) Tuesday 2.00 PM to 5.00 PM	11/11/2026 (AN) Wednesday 2.00 PM to 5.00 PM

Students are advised to note the change in the time-table and attend the semester end examinations accordingly.

(Dr. T S R CHOWDARY)
Controller of Examinations

(Dr. A V RATNA PRASAD)
Principal