

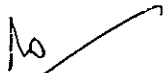
VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA

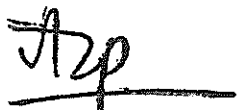
Date: 28/09/2026

NOTICE

The Semester-end Examination of B.Tech., I & II-Semester (VR20) Supplementary Examination for the following subject is rescheduled as given below.

Subject Code & Name	Actual Schedule of Examination & Date & Time	Postponement of Examination & Date & Time
20ES1105 / 2105 – Engineering Graphics	07/10/2026 (Wednesday) 9.00 AM to 12.00 Noon	08/10/2026 (Thursday) 9.00 AM to 12.00 Noon


(Dr. T S R CHOWDARY)
Controller of Examinations


(Dr. A V RATNA PRASAD)
Principal