



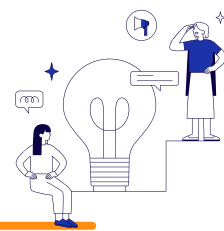
NEWSLETTER

EVENTS ORGANIZED IN THE DEPT

6 WEEKS “SUMMER INTERNSHIP ON RF ANTENNA MODELLING AND ANALYSIS USING EM TOOLS

The Department of Electronics and Communication Engineering at Velagapudi Ramakrishna Siddhartha School of Engineering, SAHE organized a “6-week Summer Internship Program entitled “RF Antenna Modeling and Analysis using EM Tools”, scheduled from 19th May 2025 to 28th June 2025. This specialized internship was designed to provide in-depth knowledge and hands-on training in the domain of RF antenna design and electromagnetic simulation tools. The program was coordinated by Dr. K. Kumar Naik (Professor, ECE, SAHE), Dr. K. Vara Prasad (Assistant Professor, ECE, SAHE), and Mrs. B. Alekya (Assistant Professor, ECE, SAHE). The course was tailored for students interested in exploring RF technologies and electromagnetic (EM) analysis, particularly useful for those aiming for careers or higher studies in 5G, IoT, or wireless communication systems. Sponsored by the Siddhartha Academy of General & Technical Education, the internship promised to offer participants a solid foundation in antenna theory, practical design methods, and EM simulation using advanced software tools. This was an excellent opportunity for students to enhance their technical skills and industry readiness in a rapidly evolving and in-demand field.





SUMMER INTERNSHIP ON VLSI DESIGN

The Summer Internship on VLSI Design 2025 was successfully conducted by Dr G Kishore Kumar, Dr CH Naga Raghuram, Assistant Professors Dept of ECE from 19th May 2025 to 28th June 2025 to provide in-depth practical exposure to undergraduate students in the field of VLSI Design.



Around 68 Participants registered the event. This intensive 6-week program aimed to bridge the gap between academic learning and industry demands in the semiconductor domain. The main objective of the program was to introduce participants to the state-of-the-art EDA tools used in VLSI industry and provide hands-on training on RTL-to-GDSII flow using Synopsys EDA tools.



This also helped the students in Analog Circuit Design and Layout using Cadence Virtuoso. Participants gained practical exposure to industry-grade VLSI design environments and enhanced understanding of digital and analog design flows.



ONE WEEK WORKSHOP ON “DESIGN AND MANUFACTURING OF PCB”

The Department of Electronics and Communication Engineering, Siddhartha Academy of Higher Education conducted a workshop from 10th June to 14th June 2025 to the non-teaching faculty of ECE, EEE and EIE of VRSSOE. The event was convened by Dr. D Venkata Rao, Dean of the School of Engineering and Head of the ECE Department at VRSSOE, with coordination by Dr. B Lakshmi Sirisha, Associate Professor, and Mr. G Hema Kumar, Senior Assistant Professor, ECE, VRSSOE. The resource persons included Mr. V Govinda Rao, Mr. B Sambasiva Rao, and Mr. Kishore from Microlink Information Technologies, Vijayawada.



Organized by
Department of Electronics & Communication Engineering
Velagapudi Ramakrishna Siddhartha School of Engineering

One Week Workshop on :
DESIGN AND MANUFACTURING OF PCB

Date: 10th-14th June 2025 Time: 2:00 PM
Venue: Analog Electronics and Circuits Lab, ECE

Resource person:
Mr. V. Govinda Rao
Founder,
Microlink Information Technologies,
Vijayawada.

CONVENOR:
Dr. D. Venkata Rao
Dean-School of Engineering
HoD, ECE Dept., SAHE

Faculty Co-Ordinators:
Dr. B. Lakshmi Sirisha
Associate Professor, ECE, VRSSOE
Mr. G. Hema Kumar
Sr. Assistant Professor, ECE, VRSSOE

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ACADEMY OF HIGHER EDUCATION
DEEMED TO BE UNIVERSITY
An Institution Affiliated to JNTU, Kakinada & UGC, New Delhi

The workshop aimed to teach participants about different types of PCBs and their applications in electronics, applying correct component placement, routing, and trace design techniques to ensure functionality and manufacturability, producing Gerber files for PCB fabrication, and practicing design ideas along with troubleshooting practical issues through assembling and testing a PCB.



Publications



Journals:

- Shahnaz Fatima and Shri Ramtej Kondamuri. "Machine Learning in Future Wireless Communications: Innovations, Applications, and Open Challenges." *Wireless Personal Communications* (2025). <https://doi.org/10.1007/s11277-025-11770-y>.
- Venkata Sainath Gupta Thadikemalla, Shri Ramtej Kondamuri, Venkateswarlu Alapati and Abhay S. Gandhi (2025). A comparison of compressive sensing based reconstruction algorithms used for data loss recovery in SHM applications - Simulation and experimental demonstration. *Earthquakes and Structures*, 29 (1), 17-26. DOI: <https://doi.org/10.12989/eas.2025.29.1.017>
- Choudhury, Ashok, Shobha Sharma, Venkata Sainath Gupta Thadikemalla, Archana Rastogi, Tapan Gandhi, and Shiv Kumar Sarin. "THU-067 A simple lab based machine learning model (FAET) predicts advanced Liver Fibrosis more accurately in MASLD patients." *Journal of Hepatology*, 82, S272-S273. [https://doi.org/10.1016/S0168-8278\(25\)00880-3](https://doi.org/10.1016/S0168-8278(25)00880-3)

Publications with Students.....

Conferences:

- B. S. Spandana, J. Keerthana, T. Rajitha and K. S. Ramtej, "Enhanced Central Sleep Apnea Detection Using a Hybrid Deep Learning Model with EEG Signals," 2025 7th International Conference on Signal Processing, Computing and Control (ISPCC), SOLAN, India, 2025, pp. 758-763, doi: 10.1109/ISPCC66872.2025.11039602.



MEMORANDUM OF UNDERSTANDING (MOUs) FOR THE ACADEMIC YEAR 2024-25

Sl. No.	Name & Address of the Organization	From	Till	Validity
1	Lam Research Corporation	01-07-2025	01-07-2030	5 years
2	Dot Workplace Solutions Pvt. Ltd., Bengaluru	23-05-2025	23-05-2026	1 year
3	Enthu Technology Solutions India Pvt. Ltd, Coimbatore	15-05-2025	15-05-2028	3 years
4	M/s BSNL, Vijayawada Telecom District	12-05-2025	12-05-2028	3 years
5	Airports Authority of India, Vijayawada	17-04-2025	17-04-2030	5 years
6	MMRFIC Technology Private Limited, Hyderabad	10-04-2025	10-04-2030	5 years
7	Pacific Electronics, Telangana	15-03-2025	15-03-2026	1 year
8	International Council on Systems Engineering - India South Chapter	23-01-2025	22-01-2028	3 years
9	Tech Mythree, Rajahmundry	19-12-2024	18-12-2026	2 years
10	Avantel Limited, Visakhapatnam, Andhra Pradesh.	30-12-2024	30-12-2027	3 years
11	Microlink Information Technologies, Vijayawada	12-11-2024	11-11-2029	5 years
12	Tessolve Semiconductor Test Engineering Lab	01-11-2024	01-11-2027	3 years
13	Efftronics Systems Pvt. Limited, Vijayawada, India	26-07-2024	25-07-2025	1 year



PLACEMENTS HIGHLIGHTS (2024-25)

S.No.	COMPANY NAME	STUDENTS SELECTED	PAY SCALE (LPA)
1	NXP SEMICONDUCTORS	1	23 .00
2	AVANTEL	34	5.00
3	ANALOG PORT	3	4.00
4	VEM TECHNOLOGIES	1	4.00
5	CADSYS	18	3.52
6	CAPGEMINI	1	7.50
7	TCS DIGITAL	2	7.20
8	TEACHNOOK	34	6.00
9	ACCENTURE	49	4.50
10	RINEX TECHNOLOGIES	15	4.50
11	CAPGEMINI	11	4.25
12	LTI MINDTREE	5	4.05
13	CTS GENC	4	4.00
14	TCS PEGA	2	3.86
15	INFOSYS	3	3.60
16	TCS NINJA	64	3.36
	TOTAL OFFERS	247	
	TOTAL STUDENTS PLACED	191	



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