

V R SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA – 520 007

Date: 14/08/2025

B.TECH MINORS TIME TABLE FOR B.TECH., VI-SEMESTER (VR20)
EXAMINATIONS, AUGUST -2025 (2022 BATCH)

Time: 2.00 PM to 5.00 PM

DATE	CE / ECE / EEE / EIE / ME
25/08/2025 Monday	20CSM6701 – Fundamentals of Operating Systems 20AI&DSM6701 – Artificial Intelligence in Automation

B.TECH HONORS TIME TABLE FOR B.TECH., VI-SEMESTER (VR20)
EXAMINATIONS, AUGUST-2025 (2022 BATCH)

Time: 2.00 PM to 5.00 PM

DATE	CE	AI&DS	CSE	CSE (AI&ML)	ECE	IT
25/08/2025 Monday	20CEH6801A – Traffic Analysis & Design	----- 20AI&DSH6801B / 20CSH6801F – Backend Technologies	20CSH6801A – Artificial Neural Networks	20AI&MLH6801 – High Performance Computing	20ECH6801E – Communication Buses and Interfaces	20ITH6801A – Social Media Mining 20ITH6801B – Penetration Testing and Vulnerability Assessment

(Dr. T S R CHOWDARY)
Controller of Examinations

(Dr. A V RATNA PRASAD)
Principal