

K.Umamaheswari



Designation : Assistant Professor
E-Mail ID : uma.patchava@vrsiddhartha.ac.in
Contact at : Department of Electronics and Instrumentation Engineering,
 SAHE, Deemed to be university, VR School of Engineering
 Kanuru, Vijayawada, Andhra Pradesh 520007.

Education

M.Tech (Digital systems and computer Electronics)
 2012, NEC,(JNTU Anantapur)

B.Tech (Electronics and Instrumentation Engineering)
 1998, Bapatla Engineering college, Nagarjuna University.

Experience

Period	Designation	Institution / Organization	Key Responsibilities
2010 till date	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering College	Department CMS & CAMPX coordinator, Linear Integrated Circuits Lab In charge
2008-2010	Assistant Professor	Priyadarshini Engineering College, Nellore	
8-3-2000 to 9-10-2000	Assistant Professor	PVPSIT Engineering College, Kanuru, Vijayawada.	

Research Interests

Broad Area of Research :
 Wireless sensor networks

Honors and Awards

Courses Taught

S.No	Course Name	Level (UG/PG)
1	Digital systems and circuits	UG
2	Linear IC Applications	UG
3	Electronic Devices and circuits	UG
4	Wireless sensor networks	UG
5	Industrial Data Communication Networks.	UG
6	Data communication systems and networking.	UG
7	Pulse and digital circuits	UG
8	Analog Electronics	UG
9	Industrial instrumentation	UG

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?user=Z7sh0RcAAAAJ&hl=en
Scopus	https://www.scopus.com/authid/detail.uri?authorId=57193781109
ORCID	https://orcid.org/0000-0001-5297-725X
LinkedIn	https://www.linkedin.com/in/uma-maheswari-bb331826

Recognized Research Supervisor

S.No.	Department	University
01.		

Funded Research Projects

S.No.	Type	Title	Status	Funding Agency	Amount (INR)	Duration

Administrative Roles

S.No	Role (Dept level)	Institution	Duration	Responsibilities
1	Timetables coordinator	SAHE, Deemed to be university	2017-Till date	Time table preparation
2	Department CMS & CAMPX coordinator	SAHE, Deemed to be university	2015- Till date	Faculty allocation to subjects. Support faculty and students in accessing and using the CMS effectively.
3	Linear Integrated Circuits Lab In charge	SAHE, Deemed to be university	2014- Till date	Ensure the lab is well-equipped, organized, Design new experiments, prepare lab manuals, Procure the equipment as per curriculum requirements.

Certifications/MOOC Courses Completed

S.No.	Title	Platform	Duration	Year
1.	System Design Through Verilog	NPTEL	8 weeks	2024
2.	Digital Circuits	NPTEL	12 weeks	2023
3.	Introduction To Industry 4.0 and Industrial Internet of Things	NPTEL	12 weeks	2022
4.	Design for internet of things	NPTEL	8 weeks	2021
5.	Introduction to Machine Learning	NPTEL	12 weeks	2020
6.	Introduction to Internet of Things	NPTEL	12 weeks	2019
7.	Introduction to Research	NPTEL	8 weeks	2018
8.	Integrated Circuits, MOSFETs, Op-Amps and their Applications	NPTEL	12 weeks	2018

Consultancy / Industry Projects

Completed	
On-going	
Applied	

Patents

S.No	Title	Status	Application No.	Year
1.	Patient Medical Database Access System Using Fingerprint Over a Cloud Computing	Published	202341001708 A	03/02/2023
2.	A wire less networking of medical equipment on mobile application for paper less clinic	Granted	2020103799	3/03/2021

Ph.D

Pusuing (KL,Deemed to be university)

Invited Talks

No. of Invited Talks

Professional Service / Reviewer

- Reviewer in Clustering Computing, Springer

• Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	16
International / National Level Seminars	00 / 02
International / National Level Conferences	21/ 01
Workshops	16
Instructor Led / Self-Paced Courses	12
Webinars	10

Professional Bodies Membership Details

S.No.	Membership No.	Society Name
1	LM66943	Life member of Indian Society for Technical Education (ISTE).
2	LM03734	Life member of Indian Society of Systems For Science& Engineering(ISSE).

K. Umami

Date 09-09-25

(K.Umamaheswari)