Dr S N V S K Chaitanya

Designation   : Assistant Professor
E-Mail ID   : snvskchaitanya@vrsiddhartha.ac.in
Contact at   : Department of Electrical and Electronics Engineering,
Velagapudi Ramakrishna Siddhartha Engineering College,
Kanuru, Vijayawada, Andhra Pradesh 520007.

Education

Ph. D (Electrical and Electronics Engineering)
2024, Annamalai university, Chidambaram, Tamil Nadu

M.Tech (Power Systems)
2008, Acharya Nagarjuna university, Kakinada

B.Tech (Computer Science and Engineering)
2005, Jawaharlal Nehru Technological University, Namburu

Experience

Period	Designation	Institution / Organization	Key Responsibilities
2009-Till Date	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering College	Placements Coordinator, U.G/PG guidance, research publications, HVE Lab In-Charge, IV NBA/ VI NAAC coordination.
2008-2009	Trainee Service Engineer	ProMax PMC LTD	

Research Interests

Broad Area of Research:

Multi-Objective Optimal Reactive Power Dispatch (MORPD), MORPD with renewable energy uncertainty modelling, Demand response strategies with RES integration in MORPD, Coordinated control of multiple FACTS devices in MORPD

Honors and Awards

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Courses Taught

S.No	Course Name	Level (UG/PG)
1	Utilization of electrical power	UG
2	Switchgear and protection	UG
3	High Voltage Engineering	UG
4	Computer Aided Power Systems	UG
5	Circuit Theory	UG
6	Basic Electrical engineering	UG
7	Industrial Electrical Systems	UG
8	Digital signal and processing	UG
9	Computer Aided Power Systems	PG
10	Advanced power system Protection	PG
11	High Voltage Engineering	PG
12	Power system operation & control	PG

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?user=FdU7FoAAAAAJ&hl=en
Scopus	https://www.scopus.com/authid/detail.uri?authorId=58868819700
ORCID	https://orcid.org/0000-0002-8548-8361
dblp	Researcher Search - Web of Science Researchers
Publons	

Research Gate	Home Feed	ResearchGate
LinkedIn	(25) Dr. N V S K CHAITANYA SELA	LinkedIn

Recognized Research Supervisor

S.No.	Department	University
01.		

Funded Research Projects

S.No.	Type	Title	Status	Funding Agency	Amount (INR)	Duration
01.						

Administrative Roles

S.No.	Role	Institution	Duration	Responsibilities
1				

Certifications/MOOC Courses Completed

S.No.	Title	Platform	Duration	Year
1.				
2.				

Consultancy / Industry Projects

Completed	
On-going	
Applied	

Patents

S.No	Title	Status	Application No.	Year
1.				

Ph.D

Awarded	00
Guiding	00

Invited Talks

No. of Invited Talks	00

Professional Service / Reviewer

- Reviewer in Computers & Electrical Engineering, Elsevier
- .

Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	13
International / National Level Seminars	06 / 02
International / National Level Conferences	02 / 06
Workshops	10
Instructor Led / Self-Paced Courses	14
Webinars	10

Professional Bodies Membership Details

S.No.	Membership No.	Society Name
1.	M-1618118	The Institution of Engineers (AIE)

16/9/2025

Dr. SNVSK Chaitanya

(Your Name & Signature with Date)

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