

Dr. Binay Binod Kumar



Designation : Assistant Professor
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Contact at : Department of Electronics and Communication
Engineering, Velagapudi Ramakrishna Siddhartha
Engineering College, Kanuru, Vijayawada, Andhra Pradesh
520007.

Education

Ph. D (VLSI Design)

2024, National Institute of Technology Jamshedpur

M.Tech (Electronics and Communication Engineering)

2015, Deenbandhu Chhotu Ram University of Science and Technology, Sonipat

B.Tech (Electronics Engineering)

2008, Bharati Vidyapeeth College of Engineering, Pune

Experience

Period	Designation	Institution / Organization
2025-Till Date	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering College
2018-2019	Assistant Professor	Vishveshwarya Group of Institutions
2015-2017	Assistant Professor	Gateway Institute of Engineering and Technology
2012-2013	Lecturer	Gateway Institute of Engineering and Technology
2008-2012	O & M Engineer	RN Infra Communication Pvt. Ltd

Research Interests

Broad Area of Research :

Semiconductor device simulation specializing in ZnO-based TFTs, Fabrication of thin film nanomaterials, TFT based biosensors, Artificial intelligence neural network for image processing.

Courses Taught

S.No	Course Name	Level (UG/PG)
1	Analog Electronics	UG
2	Digital Electronics	UG
3	Microprocessor & Microcontroller	UG
4	Network Theory	UG
5	Control System	UG

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?hl=en&user=h4iZwEkAAAAJ
Scopus	www.scopus.com/authid/detail.uri?authorId=58928269100
ORCID	0000-0002-3338-8377
Research Gate	https://www.researchgate.net/profile/Binay-Kumar-13
LinkedIn	https://www.linkedin.com/in/dr-binay-binod-kumar-2a70b8154/

Recognized Research Supervisor

S.No.	Department	University
1	Electronics and Communication Engineering	Deenbandhu Chhotu Ram University of Science and Technology

Funded Research Projects

Completed	
On-going	
Applied	

Administrative Roles

S.No.	Role	Institution	Duration	Responsibilities
1	Nano Fabrication Lab Incharge	VRSEC	03/09/2025-Present	Train researchers and students in nanofabrication techniques. Maintain cleanroom standards for precision fabrication.

Consultancy / Industry Projects

Completed	
On-going	
Applied	

Patents

Granted	
Published	01
Applied	

Invited Talks

No. of Invited Talks	0
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Professional Service / Reviewer

- Sensors and Actuators A: Physical, Elsevier
- IEEE Sensor
- Transactions on Electrical and Electronic Materials, Springer

Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	03
International / National Level Seminars	00 / 03
International / National Level Conferences	2 / 00
Workshops	08
Instructor Led / Self-Paced Courses	0
Webinars	2

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