

Dr.Pratikhya Raut



Designation : Assistant Professor
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Education

Ph. D (Microelectronics and VLSI Design)

2023., VIT-AP University, Andhra Pradesh

M.Tech (VLSI and Embedded System Design)

2017, BPUT, Odisha

B.Tech (Electronics and Communication Engineering)

2014, BPUT, Odisha

Experience

Period	Designation	Institution / Organization	Key Responsibilities
July 2023- Till Date	Assistant Professor	Velagapudi Ramakrishna Siddhartha School of Engineering	Curriculum design, Ph.D./PG /UG guidance, research publications, academic leadership, NBA/NAAC /ISO coordination.
Mach 2019- September 2020	GIS Mapping Analyst	Cognizant Technology Solutions, Hyderabad	Performing quality assurance (QA) and quality control (QC) on map layers (both vector and raster) and Serving as a digital mapping domain expert

Research Interests

Broad Area of Research:

VLSI Design, Semiconductor Device Modeling, Microelectronics, Junctionless FET , Tunnel FET , FinFET, Biosensors Design, Low Power Devices, TCAD Simulation

Honors and Awards

- Received Best Research Award at University Level 2022-23 at VIT-AP University.
- Received Best Research Poster Presentation **V-SAMSODH 2022** at VIT-AP University.

Courses Taught

S.No	Course Name	Level (UG/PG)
1	Basic Electronics	UG
2	Digital Design with FPGA	UG
3	Digital Circuit Design	UG
4	Semiconductor Device Physics	UG
5	AI Tools and Applications	UG

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?hl=en&user=2PxNU9AAAAAJ
Scopus	https://www.scopus.com/authid/detail.uri?authorId=57226812484
ORCID	https://orcid.org/0009-0004-8332-0604
Publons	https://www.webofscience.com/wos/author/record/KMA-1192-2024
Research Gate	https://www.researchgate.net/profile/Pratikhya-Raut
LinkedIn	https://www.linkedin.com/in/dr-pratikhya-raut-50ab61141/?originalSubdomain=in

Recognized Research Supervisor

S.No.	Department	University
01.	Electronics and Communication Engineering	Siddhartha Academy of Higher Education, Deemed to be University, Vijayawada

Funded Research Projects

S.No.	Type	Title	Status	Funding Agency	Amount (INR)	Duration
01.	Funded	AI-Driven Optimization of CZTS Solar Cell Fabrication for Enhanced Efficiency in Smart City Applications	Applied	ANRF	55 Lakhs	3

Administrative Roles

S.No.	Role	Institution	Duration	Responsibilities
1.	Student Counsellor	SAHE	2023-Present	Mentoring students in their Academic Performance and other co-curricular Activities

Funded Research Projects

Completed	
On-going	
Applied	01

Consultancy / Industry Projects

Completed	
On-going	
Applied	

Patents

Granted	
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Published	
Applied	

Invited Talks

No. of Invited Talks	01
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Professional Service / Reviewer

- Microsystem Technologies
- Silicon
- Discover Electronics
- ECS Journal of science & Technology
- Microelectronics Journal
- Semiconductor Science and Technologies
- IEEE sensors journal
- IEEE Access
- IEEE Transactions on Electron Devices

Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	11
International / National Level Seminars	10
International / National Level Conferences	08
Workshops	110
Instructor Led / Self-Paced Courses	01
Webinars	5



10-09-2025

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