

Dr SUNKARA TEENA MRUDULA



Designation : Assistant Professor
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Contact at : EC355, Department of ECE,
 Velagapudi Ramakrishna Siddhartha School of Engineering,
 SAHE, Kanuru, Vijayawada, Andhra Pradesh 520007.

Education

Ph. D (Electronics and Communication Engineering)

2025, Jawaharlal Nehru Technological University, Anantapur

M.Tech (VLSI DESIGN)

2013, Intellectual Engineering College, Jawaharlal Nehru Technological University, Anantapur

B.Tech (Electronics and Communication Engineering)

2008, Sri Krishna Devaraya Engineering College, Jawaharlal Nehru Technological University, Hyderabad

Experience

Period	Designation	Institution / Organization	Key Responsibilities
2022- tilldate	Assistant Professor	Velagapudi Siddhartha Engineering College	Class teacher Proctor NAAC Criteria IV coordinator NBA Criteria VI coordinator ISO 66E file coordinator Lab In Charge Teaching Research UG Guidance
2019-22	Assistant Professor	DVR & Dr HS MIC College of Technology	Proctor UG Project coordinator ISO File I incharge

			UG Guidance Teaching Research
2018-2019	Assistant Professor	SriKrishna Devaraya Engineering College	UG Guidance Teaching Research
2017-2018	Assistant Professor	Intell Engineering College	UG Guidance Teaching Research
2014-2017	Assistant Professor	SriKrishna Devaraya Engineering College	UG and PG Guidance Teaching Research Lab Incharge
2008-2011	Assistant Professor	SriKrishna Devaraya Engineering College	Teaching Lab Incharge

Research Interests

Broad Area of Research :
VLSI Signal Processing

Honors and Awards

Nil

Courses Taught

S.No	Course Name	Level (UG/PG)
1	VLSI DESIGN	UG
2	DSD USING PLDS	UG
3	DIGITAL CIRCUIT DESIGN	UG
4	PROBABILITY THEORY AND STOCHASTIC PROCESSES	UG

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?user=X99mMsYAAAAJ&hl=en
Scopus	https://www.scopus.com/authid/detail.uri?authorId=57211212735
ORCID	https://orcid.org/0000-0001-9473-0802

Recognized Research Supervisor

S.No.	Department	University
Nil		

Funded Research Projects

S.No.	Type	Title	Status	Funding Agency	Amount (INR)	Duration
-						

Administrative Roles

S.No.	Role	Institution	Duration	Responsibilities
-				

Certifications/MOOC Courses Completed

S.No.	Title	Platform	Duration	Year
1.	SYSTEM DESIGN THROUGH VERILOG	NPTEL	8 WEEKS	2024
2.	DIGITAL CIRCUITS	NPTEL	12WEEKS	2019

Consultancy / Industry Projects

Completed	-
On-going	-
Applied	-

Patents

S.No	Title	Status	Application No.	Year
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Ph.D

Awarded	-
Guiding	-

Invited Talks

No. of Invited Talks	-
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Professional Service / Reviewer

- Reviewer in PATTERN RECOGNITION

Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	15
International / National Level Seminars	5
International / National Level Conferences	5
Workshops	3
Instructor Led / Self-Paced Courses	3
Webinars	2

Professional Bodies Membership Details

S.No.	Membership No.	Society Name
1.	AM17 1842-0	INSTITUTE OF ENGINEERS

(Your Name & Signature with Date)
