

LAKSHMI SOWJANYA KONERU



Designation : Assistant Professor
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Contact at : EC357, Department of Electronics and Communication Engineering, Velagapudi Ramakrishna Siddhartha Engineering College, Kanuru, Vijayawada, Andhra Pradesh 520007.

Education

Ph. D, Pursuing (Electronics and Communication Engineering)

National Institute of Technology, Warangal(NITW)

M.Tech (VLSI System Design)

2009, VNR VignanaJyothi Institute of Engineering and Technology, affiliated to JNTUH

B.Tech (Electronics and Communication Engineering)

2006, Nizam Institute of Engineering & Technology, affiliated to JNTUH .

Experience

Period	Designation	Institution / Organization	Key Responsibilities
August 2022 to till date	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering College	Department cultural Coordinator, Athisaya club coordinator
2010-2022	Assistant Professor	Dhanekula Institute of Engineering & Technology, Ganguru	
2006-2007	Lecturer	K. L. College of Engineering, vaddeswaram	

Research Interests

Broad Area of Research :

Digital VLSI / Approximate Computing

Honors and Awards

- Received the Best Paper Award for IEEE Conference paper titled “**Compressor-Based Approach for Power-Optimized Error-Tolerant High Accurate Approximate Multipliers Design and Implementation**” in the **VLSI Technology and Embedded Systems Track** at 10th International Conference on Signal Processing and Communication (ICSC), held at IIIT, Noida, from February 20-22, 2025.

Courses Taught

S.No	Course Name	Level (UG/PG)
1	Pulse & Digital Circuits	UG
2	Digital Electronics	UG
3	Analog Electronics	UG
4	Electronic System Design	UG
5	Digital System Design using Programmable Devices	UG
6	VLSI Technology	PG
7	CAD for HDL	PG

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.co.in/citations?user=kW3bLMMAAAAJ&hl=en
ORCID	HYPERLINK "https://orcid.org/my-orcid?orcid=0000-0002-6095-6122"
LinkedIn	HYPERLINK "https://www.linkedin.com/feed/?trk=onboarding-landing"

Recognized Research Supervisor

S.No.	Department	University
NA		

Funded Research Projects

S.No.	Type	Title	Status	Funding Agency	Amount (INR)	Duration
NIL						

Administrative Roles

S.No.	Role	Institution	Duration	Responsibilities
1	Department cultural coordinator	VRSEC	2024-Present	To encourage department students to participate in Cultural events.

Certifications/MOOC Courses Completed

S.No.	Title	Platform	Duration	Year
1.	Digital Electronic Circuits	NPTEL	12 Weeks	2025

Consultancy / Industry Projects

Completed	NIL
On-going	NIL
Applied	NIL

Patents

S.No	Title	Status	Application No.	Year
NIL				

Ph.D

Awarded	NA
Guiding	NA

Invited Talks

No. of Invited Talks	NIL
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Professional Service / Reviewer

- NIL

Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	08
International / National Level Seminars	00/ 04
International / National Level Conferences	05/02
Workshops	03
Instructor Led / Self-Paced Courses	03
Webinars	02

Professional Bodies Membership Details

S.No.	Membership No.	Society Name
1.	LM03789	INDIAN SOCIETY OF SYSTEMS FOR SCIENCE & ENGINEERING (ISSE)
2.	LM116945	Indian Society for Technical Education (ISTE)

(K.Lakshmi Sowjanya)
