



V R Siddhartha School of Engineering Siddhartha Academy of Higher Education (An Institution Deemed to be University) Department of ECE



Volume 13

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NEWSLETTER EVENTS ORGANIZED IN THE DEPT

ECE DISTINGUISHED ALUMNI LECTURE SERIES ON “AI-POWERED FUTURE”

The Department of ECE organized a Distinguished Alumni Lecture on “AI-Powered Future” on 26th August 2025. The guest speaker, Mr. Chetan Velicheti (Batch 2003–2007), AI Tester at AICPA CIMA, London (UK), delivered an engaging talk on the transformative role of Artificial Intelligence across industries. He highlighted AI’s impact on technology, business, healthcare, and education, stressing the importance of ethical practices and data-driven decision-making. Drawing from his global experience, he advised students to strengthen their fundamentals in programming, mathematics, and problem-solving, while nurturing critical thinking and innovation. The session inspired students to explore research, interdisciplinary applications, and entrepreneurship, motivating them to contribute meaningfully to the AI revolution.

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Established by Siddhartha Society of India, Vijayawada, A.P.

in Association with

Student Forum

Department of **Electronics & Communication Engineering**

ECE Distinguished Alumni Lecture Series on
AI - POWERED FUTURE

Date:
26th August 2025

Time:
11.15AM

Invited Speaker:
Mr. CHETAN VELICHETI
AI Tester, AICPA CIMA
London, United Kingdom
(2003-2007) Alumnus



Venue: ECE-Seminar Hall

Targeted Audience:
III/IV and IV/IV B.Tech ECE Students

Conveners:
Dr. D. Venkata Rao
Dean, VRS School of Engineering
BOD-ECE

Dr. M. Padmaja
Professor, ECE, VRSSE

Faculty Coordinators:
Mrs. K. V. Ratna Prabha
Assistant Professor, ECE, VRSSE
Dept. Alumni Coordinator

Dr. V. Saritha
Assistant Professor, ECE, VRSSE
Dept. IEEE Coordinator

Student Coordinators:
Ramya Sree
III/IV B.Tech

Dervish
III/IV B.Tech



ECE DISTINGUISHED ALUMNI LECTURE SERIES ON “CAREER PROSPECTS IN VLSI”

The Department of ECE conducted a Distinguished Alumni Lecture on “Career Prospects in VLSI” on 23rd August 2025. The session was delivered by Mr. Manchala Eswar Kumar (Batch 2013–2016), SoC Logic Design Engineer at Intel Technology India Pvt. Ltd., Bengaluru. He spoke about the vast career opportunities in VLSI and semiconductor technology, emphasizing the importance of strong fundamentals in digital design, CMOS, and programming languages like Verilog and VHDL. He advised students to sharpen problem-solving skills, pursue internships, and engage in industry-relevant projects to stay competitive. The session motivated students to explore VLSI as a dynamic and rewarding career path.

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Department of **Electronics & Communication Engineering**

ECE Distinguished Alumni Lecture Series on
CAREER PROSPECTS IN VLSI

Date:
23rd August 2025

Time:
11.00AM

Invited Speaker:
Mr. MANCHALA ESWAR KUMAR
SoC Logic Design Engineer
Intel Technology India Pvt. Ltd.
Bengaluru, Karnataka, India
(2013-2016) Alumnus



Venue: ECE - 328

Targeted Audience:
III/IV and IV/IV B.Tech ECE Students

Conveners:
Dr. D. Venkata Rao
Dean, VRS School of Engineering
BOD-ECE

Dr. M. Padmaja
Professor, ECE, VRSSE

Faculty Coordinators:
Dr. S.K. Fayaz Ahmed
Associate Professor, ECE, VRSSE

Mrs. K. V. Ratna Prabha
Assistant Professor, ECE, VRSSE
Dept. Alumni Coordinator

Student Coordinators:
Ramya Sree
III/IV B.Tech

Dervish
III/IV B.Tech



ECE DISTINGUISHED ALUMNI LECTURE SERIES ON “CAREER PROSPECTS IN VLSI”

The Department of Electronics and Communication Engineering, SAHE, in association with SAGTE and the Antenna Research Group, organized a National Seminar on DRAs on 4th August 2025 at the ECE Seminar Hall. The event aimed to acquaint faculty, researchers, and final-year students with the theoretical and experimental advancements in DRAs, focusing on their role in low-loss, broadband, and IoT communication systems.

The keynote address was delivered by Dr. Bratin Ghosh, Professor, IIT Kharagpur, who shared insights on advanced antenna concepts such as full-wave analysis, Green's function formulation, slot-coupled, and probe-fed configurations. His lecture highlighted the significance of DRAs in next-generation wireless technologies.

Organized by
Department of
Electronics & Communication Engineering

One Day National Seminar on:
**FULL WAVE ANALYSIS AND DESIGN
OF DIELECTRIC RESONATOR ANTENNAS
FOR LOW LOSS BROAD BAND
AND IOT APPLICATIONS**

Date: 04th August 2025 **Time:** 09:30 AM **Venue:** ECE Seminar Hall: (EC324)

Conveners:
Dr. D. Venkata Rao
Dean School of Engineering,
HOD-ECE, SAHE

Dr. A. Jhansi Rani
Professor, ECE, SAHE
& Antenna Research Group Head

Faculty Coordinators:
Dr. Ch. Raghavendra
Assistant Professor,
ECE, SAHE

Mrs. K. Sneha
Assistant Professor,
ECE, SAHE

Student Co-ordinators:
E. Maneesh
IV/IV B.Tech ECE-A

D. Krishna Raju
IV/IV B.Tech ECE-A

Registration Link: <https://forms.gle/JXXGCBpyHeFGQnjF9>

Targeted Audience: Antenna Research group faculty and IV/IV B.Tech ECE Students

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Sponsored by
SAGTE &
Antenna Research Group

Dr. BRATIN GHOSH
Professor,
Electronics & Electrical Communication Engg.,
IITKGP

JOURNALS:

- Pratikhya Raut, D. K. Panda and A. K. Goyal, "A Comprehensive Review on Next-Generation Modeling and Optimization for Semiconductor Devices," in IEEE Access, vol. 13, pp. 123724-123742, 2025, doi: 10.1109/ACCESS.2025.3587721.
- Sikhamani, T., Panda, D.K. & Pratikhya Raut, Performance analysis of nanoscale organic field-effect transistors with respect to different device parameters. Microsystem Technologies (2025). <https://doi.org/10.1007/s00542-025-05901-5>
- K. Sneha and V. Agarwal, "Design and Analysis of Compact Four-Port Fractal-Loaded MIMO Antenna for 5-g Application," International Journal of Communication Systems 38, no. 13 (2025): e70206, <https://doi.org/10.1002/dac.70206>.

CONFERENCES:

- S. Vujji, P. B. Sri Harika, A. Jahnavi, V. S. G. Thadikemalla and S. Sharma, "A Machine Learning based Approach for Liver Fibrosis Diagnosis in NAFLD Using Biomarkers and Demographics," 2025 International Conference on Electronics, AI and Computing (EAIC), Jalandhar, India, 2025, pp. 1-6, doi: 10.1109/EAIC66483.2025.11101506.
- K.Sneha, T.Naga Vami, K. Aarthi, K.Kiran babu, "Design and Development of a Continuous Passive Motion(CPM) Machine for post-operative joint rehabilitation using embedded control", International Conference on Modern Sustainable Systems(CMSS 2025), August 2025.

AWARDS RECEIVED BY STUDENTS

D. Pavani, 238W1A04E8, B. Venkata Gopi Nayak, 248W5A0409 and P.Teja Naga Sai 238W1A04D8, secured First prize in IGNITE EXPO 2025 organized by Microsoft Learn student chapter at SAHE on 01-08-2025.

V R Siddhartha School of Engineering Siddhartha Academy of Higher Education (An Institution Deemed to be University) Department of ECE News Letter Editorial Board

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