

**VELAGAPUDI RAMAKRISHNA  
SIDDHARTHA ENGINEERING COLLEGE  
(AUTONOMOUS)  
VIJAYAWADA**


Date: 06/12/2025

**NOTICE**

In view of the **Campus Placements**, the Semester End Examinations for B.Tech VII-Semester (VR20 Regulation) have been **rescheduled** as follows:

Branches	Actual Commencement as per Academic Calendar	Postponement of Examination
All Branches of B.Tech., VII-Semester	15/12/2025	22/12/2025

All students are **advised to take note of the revised dates** and make necessary preparations.

  
**(Dr. T S R CHOWDARY)**  
Controller of Examinations

  
**(Dr. A V RATNA PRASAD)**  
Principal