

**VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)**

Date: 01/01/2026

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. I & II SEMESTERS SUPPLE (VR23 REGULATION)
EXAMINATIONS HELD IN DECEMBER -2025**

23PC2104D – Electrical Circuit Analysis

- NIL -

23ES2103B – Basic Electrical And Electronics Engineering

- NIL -


CONTROLLER OF EXAMINATIONS
Velagapudi Rama Krishna
SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA-520 007.A.P.


PRINCIPAL
V.R.Siddhartha Engineering College
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LIST OF REGD. NO.OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. IV –SEMESTER SUPPLE(VR23 REGULATION)
EXAMINATIONS –DECEMBER - 2025

23CE4303 - Structural Analysis

- NIL -


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