



# NEWSLETTER

## EVENTS ORGANIZED IN THE DEPT

One Day Workshop was conducted for all III year B Tech ECE and M Tech SPC students on 14.02.2025. Dr Venkatappa Reddy, Technical Specialist in Signal and Image Processing & Machine Learning, AZISTA INDUSTRIES PRIVATE LIMITED, Hyderabad



### EVENTS UNDER IETE

Under IETE, several engaging events were conducted on 16th and 17th February 2026. SIMUTECH ARENA – Wire and Win was a circuit design and simulation competition using Tinkercad, with 84 teams participating. CODE SPRINT – Think Code Win provided a platform for students to demonstrate their skills in C, Python, and DSA under real-time constraints, attracting 96 teams. MURDER MYSTERY – Digital Detective Quiz tested analytical thinking, observation skills, and teamwork in a suspense-driven format, with 200 teams participating. Concept Clash, organized by IETE ISF, challenged teams with on-the-spot themes to assess creativity and critical thinking, with 27 teams taking part.



## WORKSHOP ON MASTERING THE GATE EXAM:

The Department of Electronics and Communication Engineering (ECE), Siddhartha Academy of Higher Education, organized a one-day workshop titled “Mastering the GATE Exam” on 4th February 2026 at the ECE Seminar Hall from 9:30 AM to 5:00 PM. The workshop, conducted in association with OHM, aimed to guide students in effective

Organized by  
**Department of Electronics & Communication Engineering**

Resource Persons

- Mr. KNS Hemanth (BITS Pilani)
- Mr. T. Rakesh (IISc Bangalore)
- Mr. Goutham (IIT Madras)

Date: 4<sup>th</sup> February 2026 | Time: 9:30 AM to 5:00 PM | Venue: ECE Seminar Hall

Convener: Dr. D. Venkata Rao (Deem'd Schools, HOD-ECE) | Faculty Coordinator: Mrs. M. Sunitha (Assistant Professor, ECE)

**SIDDHARTHA ACADEMY OF HIGHER EDUCATION**  
An Institution DEEMED TO BE UNIVERSITY  
(Under Section 3 of UOD Act, 1956)  
(Recognized by Siddhartha Academy of General & Technical Education, Vijayawada, A.P.)

preparation by providing structured study strategies, conceptual clarity, exam techniques, and motivation for higher studies and PSU opportunities. The session also included teaching on Analog Electronics. The resource persons were Mr. KNS Hemanth, Mr. T. Rakesh (IISc Bangalore), and Mr. Goutham (IIT Madras).

## FACULTY INTERACTION

Dr. K. Shri Ramtej delivered a guest lecture titled “Modulation Classification: From Classical to Deep Learning Methods” on 25/02/2026 at PVP Siddhartha Institute of Technology (PVPSIT), Vijayawada.

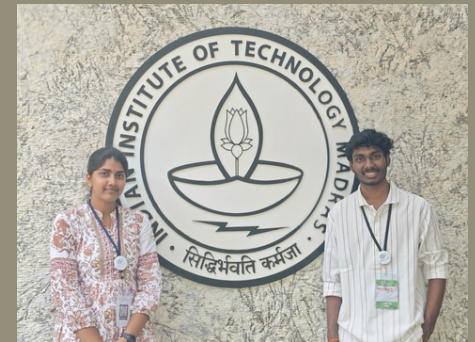


## STUDENT ACHIEVEMENTS:

- Mutnuri Pavani has been selected for Work-Based Learning (WBL) Internship Program as an Intern at C-DAC, Bangalore for a period of 6 months from 23/02/2026 to 21/08/2026, with a stipend of Rs. 10,000/- per month.



- Sai Sri Lakshmi Puttapuraju received best paper award for the paper entitled "Design and simulation of a 1D Electrochemical Glucose Sensor Using Cyclic Voltammetry and Electroanalysis in COMSOL Multiphysics at the International Conference on Artificial Intelligence in Engineering Systems, organized by Rajeev Institute of Technology, Hasan, Karnataka.
- V. Hema Asmitha (Roll No: 24EU05258) and V. Pridhvi Koushik (Roll No: 24EU05256), 2nd-year B.Tech ECE students, participated in the national-level Startup Bootcamp organized by the Entrepreneurship Cell IIT Madras during E-Summit 2026 IIT Madras.



Representing their startup idea, they cleared the first two screening rounds and reached the finals in the Energy and Sustainability sector. Competing with 46 innovators nationwide, they pitched to venture capitalists and industry experts and secured 2nd Runner-up, showcasing their innovative engineering and problem-solving skills.

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- G. Reshma, P. S. Kumar, S. Divya and K. S. Ramtej, "Optimized CNN-Based Sound Event Classification via Bayesian Hyperparameter Search," 2025 International Conference on Electronics and Computing, Communication Networking Automation Technologies (ICEC2NT), Pune, India, 2025, pp. 1-5, doi: 10.1109/ICEC2NT65402.2025.11380082.
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- Saritha Vanka, Gowri Sri Sannihitha Vaddi, Divya Sai Vittamsetti, Sindhura Ravuri, Alekhya Bandi, Vara Prasad Kudumu, "A Design of Wider Impedance Bandwidth with Spider-Parasitic Elements Patch Antenna for Ku-Band Applications", 2025 5th International Conference on Artificial Intelligence and Signal Processing (AISP), 23 February 2026.
- R. Bhavanasi, S. S. Sathik, B. Alla and S. T. Mrudula, "Modeling and Performance analysis of Capacitive Pressure Sensor with Hexagonal Diaphragm using COMSOL Multiphysics," 2025 5th International Conference on Artificial Intelligence and Signal Processing (AISP), Vijayawada, India, 2025, pp. 1-5, doi: 10.1109/AISP68263.2025.11396266.
- Ahamed, Shaik Fayaz, and Phani Kumar Polasi. "WSN-Based Earthquake Disaster Prediction using Deep Learning Model." 2025 5th International Conference on Evolutionary Computing and Mobile Sustainable Networks (ICECMSN). IEEE, 2025.
- Sai, Alla Prudhvi, V. Narasimha Nayak, Polasi Phani Kumar, Swarna Balaji, and Kollu Kartheek. "Securing RIS-Assisted NOMA Network With Optimized Resource Allocation and Known Jamming Under Single and Multiple Eavesdropping Scenarios." In 2025 5th International Conference on Artificial Intelligence and Signal Processing (AISP), pp. 1-6. IEEE, 2025.

**PLACEMENT DETAILS-**

<b>Roll No.</b>	<b>Name of the students</b>	<b>Company name</b>	<b>Package</b>
228W1A04D7	Balla Satya Praveen Kumar	LTI Mindtree	4LPA
228W1A04H4	Bitra Nischitha	LTI Mindtree	4LPA
228W1A04J5	Kallepalli Aarthi	LTI Mindtree	4LPA
228W1A04G5	Kunireddy Venkata Sai Jyothsna	LTI Mindtree	4LPA
228W1A04C6	Vaddi Gowri Sri Sannihitha	LTI Mindtree	4LPA
228W1A04D3	Anjuri Sindhuja	LTI Mindtree	4LPA
228W1A04P5	Uppu Lakshmi Manasa	LTI Mindtree	4LPA
228W1A04F0	Golla Sai Pramodh	COGNIZENT	4LPA
228W1A0416	Jasti Chandana	COGNIZENT	4LPA
228W1A0406	Bobbadi Venkata Manisyam Pranay	COGNIZENT	4LPA
228W1A04D1	Palla Durga Prasad	COGNIZENT	4LPA
228W1A04G9	Muppana Udaya Siva Kiran	COGNIZENT	4LPA
228W1A0495	Kurakula Surya Kiran	COGNIZENT	4LPA
228W1A04B7	Sanagasetty Mohana Seershika	COGNIZENT	4LPA
228W1A04D6	Baliga Mary Rani	COGNIZENT	4LPA
228W1A04G9	Muppana Udaya Siva Kiran	COGNIZENT	4LPA
228W1A04H8	Pemmadi Manoj	COGNIZENT	4LPA
228W1A04I9	Tokala Joseph Prajwal	COGNIZENT	4LPA
228W1A04P1	Thota Naga Vamsi	COGNIZENT	4LPA
238W5A0427	Konda Nohith Kumar	COGNIZENT	4LPA
228W1A04K1	Chavitipalli Dunesh	COGNIZENT	4LPA
228W1A04L1	Golivi Guna Surya	COGNIZENT	4LPA

238W5A0412	Jampana Harshith Kumar	COGNIZENT	4LPA
238W5A0428	Modukuri Gowthami	COGNIZENT	4LPA
228W1A0419	Kama Praveen Kumar	Effectronics	3LPA
228W1A0439	Napa Teja	Effectronics	3LPA
228W1A0460	Valle Yaswanth	Effectronics	3LPA
228W1A04K5	Dhulipudi Sukhesh	Effectronics	3LPA

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