

**VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE  
(AUTONOMOUS)  
VIJAYAWADA**

Date: 21/03/2026

**NOTICE**

The Semester-end Examination of B.Tech., V & VII-Semesters (VR17) and B.Tech., I & III-Semesters (VR20) Supplementary Examinations for the following subjects is rescheduled as given below due to Ramzan.

Subject Code & Name	Actual Schedule of Examination & Date & Time	Postponement of Examination & Date & Time
<b><u>V-Semester (VR17)</u></b> 17CE3509 – Design of Concrete Structures 17CS3509 – Microprocessors & Microcontrollers	21/03/2026 (Saturday) 9.00 AM to 12.00 Noon	24/03/2026 (Tuesday) 9.00 AM to 12.00 Noon
<b><u>VII-Semester (VR17)</u></b> 17ME4705C – Mechanical Measurements	21/03/2026 (Saturday) 2.00 PM to 5.00 PM	24/03/2026 (Tuesday) 2.00 PM to 5.00 PM
<b><u>I-Semester (VR20)</u></b> 20ES1103 – Programming for problem Solving	21/03/2026 (Saturday) 9.00 AM to 12.00 Noon	31/03/2026 (Tuesday) 2.00 PM to 5.00 PM
<b><u>III-Semester (VR20)</u></b> 20AI&DS3303 - Database Systems 20CE3303 - Surveying & Geomatics 20CS3303 - Operating Systems 20AI&ML3303 - Introduction to Artificial Intelligence 20EC3303 - Microcontrollers 20EE3303 - Electrical Machines-I 20EI3303 - Digital Circuits & Systems 20IT3303 - Data Structures 20ME3303 - Basic Thermodynamics	21/03/2026 (Saturday) 2.00 PM to 5.00 PM	02/04/2026 (Thursday) 2.00 PM to 5.00 PM



(Dr. T S R CHOWDARY)  
Controller of Examinations



(Dr. A V RAFNA PRASAD)  
Principal