

Dr.Indira Damarla



Designation : Senior Assistant Professor
E-Mail ID : indira@vrsiddhartha.ac.in
Contact at : Department of Electrical and Electronics Engineering,
 Siddhartha Academy of Higher Education
 Deemed to be University,
 Kanuru, Vijayawada, Andhra Pradesh 520007.

Education

Ph. D (Electrical Engineering)

2022, Anna University, Chennai

M.E (Power Electronics and Industrial Drives)

2011, Sathyabama University, Chennai

B.Tech (Electrical and Electronics Engineering)

2005, Jawaharlal Nehru Technological University, Hyderabad

Experience

Period	Designation	Institution / Organization	Key Responsibilities
2022-till date	Assistant Professor	Siddhartha Academy of Higher Education Deemed to be University	Ph.D./UG guidance, research publications, Student Internships, FDP, Guest lectures, etc events coordinator, MOU coordinator, Machines Lab in charge, NBA/NAAC coordination.
2018-2022	Research Scholar (JRF & SRF)	Anna University, Chennai	Research, publications
2013-2018	Assistant Professor	St. Ann's College of Engineering & Technology, Chirala	Undergraduate teaching, Post graduate teaching, Project guidance for final year students, and Lab Supervision, NBA/NAAC coordination.

2009- 2013	Assistant Professor	Dr. MGR Educational & Research Institute (Dr. MGR University), Chennai	Undergraduate teaching and Lab supervision, NBA/NAAC coordination.
April 2009 to Nov 2009	Lecturer	Veltech Multitech Dr.Rangarajan Dr.Sakunthala Engineering College, Chennai	Undergraduate teaching and Lab supervision.
Feb 2008 to Dec 2008	Software Engineer	Atlas Systems	Software development in JAVA
2005-2006	Assistant Professor	Chirala Engineering College, Chirala	Undergraduate teaching and Lab supervision.

Research Interests

Broad Area of Research :

Power Converters, Switched reluctance motor drives, High gain DC-DC converters, Fast charging systems for Electric Vehicles.

Honors and Awards

- Awarded (**Best Research paper**) first prize in IEEE International Conference.
- Awarded (**Best Research paper**) first prize in International Conference.
- Received Appreciation Award for **Extraordinary Excellence in Academics** during the academic year 2010-2011 in Dr. MGR Educational & Research Institute (Dr. MGR University), Chennai
- Awarded **University Third Rank** in MASTER OF ENGINEERING

Courses Taught

S.No	Course Name	Level (UG/PG)
1.	Power Electronics	UG
2.	Industrial Drives	UG
3.	Digital Signal Processing	PG
4.	Basic Electrical and Electronics Engineering	UG
5.	Analysis and design of Converters	PG
6.	Power Electronics applications in power systems	PG

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?user=GyzThp4AAAAJ&hl=en
Scopus	https://www.scopus.com/authid/detail.uri?authorId=54919937700
ORCID	https://orcid.org/0000-0003-0096-542X
dblp	
Publons	https://www.webofscience.com/wos/author/record/AAC-8686-2022
Research Gate	
LinkedIn	https://www.linkedin.com/in/dr-indira-damarla-507956218/

Recognized Research Supervisor

S.No.	Department	University
01.	Electrical Engineering	Siddhartha Academy of Higher Education Deemed to be University
02.		
03.		
04.		

Funded Research Projects

S.No.	Type	Title	Status	Funding Agency	Amount (INR)	Duration
01.	Funded	Silicon Carbide Solutions for Next-Generation PV Inverters: Addressing Leakage and Efficiency through advanced design	Applied	CPRI	50,37,760	3 Years

Administrative Roles

S.No.	Role	Institution	Duration	Responsibilities
1.	FDP, Guest lectures etc. event coordinator	Siddhartha Academy of Higher Education Deemed to be University	2022-Present	Organizing and coordinating events such as Faculty Development Programs (FDPs), guest lectures, workshops, hackathons, seminars, and related academic/industry interaction activities.
2.	Internship coordinator	Siddhartha Academy of Higher Education Deemed to be University	2023-Present	Coordinating and facilitating summer internships and major internships for students, ensuring smooth execution and monitoring of activities.
3.	MOU coordinator	Siddhartha Academy of Higher Education Deemed to be University	2023-Present	Coordinating MOUs with industries and ensuring effective implementation and monitoring of activities under them.
4.	Electrical Machines Lab In charge	Siddhartha Academy of Higher Education Deemed to be University	2024-Present	Proper functioning, maintenance, and safety of the laboratory. Ensures availability of equipment, supports faculty and students, maintains records, and oversees procurement and inventory management.

Certifications/MOOC Courses Completed

S.No.	Title	Platform	Duration	Year
1.	Cloud Computing	NPTEL	16 Weeks	2024

2.	Introduction to Internet of Things	NPTEL	16 Weeks	2024
3.	Machine Learning for Engineering and Science Applications	NPTEL	16 Weeks	2024
4.	The Joy of Computing using Python	NPTEL	16 Weeks	2024

Consultancy / Industry Projects

Completed	
On-going	
Applied	

Patents

S.No	Title	Status	Application No.	Year
1.	Efficiency Optimization of fused converter in Wireless Power Transfer Systems	Published	2025411130 2A	2025
2.	Smart Grid Monitoring System Using AI for Real-Time Fault Detection and Load Management	Published	2024410836 50A	2024
3.	Efficient Power Converter Control Methodology for Electric Vehicle Drives	Published	2024410466 72A	2024
4.	Holistic Vehicle-to-grid (V2G) Integration with Predictive Load	Published	2023410583 73A	2023

	Balancing			
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Ph.D

Awarded	Nil
Guiding	01

Invited Talks

No. of Invited Talks	Nil
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Professional Service / Reviewer

1. Served as a **reviewer** for the paper titled “*Decentralized Sliding Mode Control for Current-Balancing in Interleaved DC/DC Buck Converters*” by E. Maximiliano Asensio, Juan Astrada, Federico Martín Serra, and Cristian Hernán De Angelo, submitted to the international journal ***e-Prime – Advances in Electrical Engineering, Electronics and Energy***, on 05 October 2024.
2. Served as a **reviewer** for the paper titled “*Anomaly Detection of Adversarial Cyber Attacks on Electric Vehicle Charging Stations*” Mr. Sagar Babu Mitikiri; Dr. V L Srinivas; Mayukha Pal' by E. Maximiliano Asensio, Juan Astrada, Federico Martín Serra, and Cristian Hernán De Angelo, submitted to the international journal ***e-Prime – Advances in Electrical Engineering, Electronics and Energy***, on 01/11/2024.
3. Served as a **reviewer** for the paper titled “*Efficiency Improvement of Interior Permanent Magnet Synchronous Motor-Based Electric Vehicle Drive Train Using Phase Shedding Control Approach*”, submitted to the international journal ***International Journal of Circuit Theory and Applications***, Wiley on 20/03/2025.
4. Served as a **reviewer** for the international journal ***Scientific Reports*** on 23/04/2025.
5. Served as a **reviewer** for the paper titled “*Notch-Filter-based Active Stabilization of DC-Link for SLL Converter used in CCR Mitigation in BLDCM Drive*”, submitted to the international journal ***International Journal of Electronics***, Taylor & Francis on 09/04/2025.

Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	15
International / National Level Seminars	04 / 03
International / National Level Conferences	05 / 02
Workshops	12
Instructor Led / Self-Paced Courses	10
Webinars	13

Professional Bodies Membership Details

S.No.	Membership No.	Society Name
1.	250101050970	Institution of Engineers

Dr.Indira Damarla

(Your Name & Signature with Date)
