

V.R.SIDDHARTHA ENGINEERING COLLEGE
DEPARTMENT OF ELECTRICAL & ELECTRONICS
A.Y::2024-25

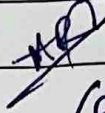
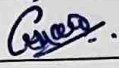
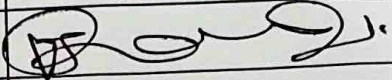
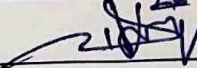
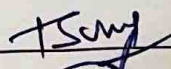
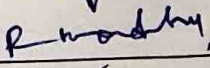
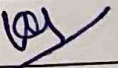
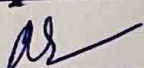
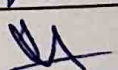
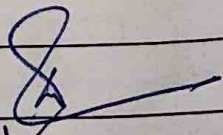
VR-23 UG BOS meeting
Date: 24-05-2025 at 10.00AM
Google meet

Agenda of the meeting:

1. To approve the proposed scheme & Syllabus of Fifth & Sixth semester, seventh & eighth semester of VR-23 regulation
2. To approve the CBCS course offered in fifth semester
3. Any other matter with the permission of the chair


HOD EEE
Dr.P.V.R.L.NARASIMHAM
Professor & Head
Dept.of Electrical & Electronics Engineering
V.R.Siddhartha Engineering College
VIJAYAWADA-520 007.

BOS MEETING HELD ON 24th MAY 2025

S.NO	Name of the Employee	Designation	SIGNATURE
1	Dr. P. V. R. L. NARASIMHAM	Prof. & HOD	
2	Dr. A.RAMADEVI	Prof	
3	Smt. S. V. R. L. KUMARI	Assoc. Prof.	
4	Dr. G. SRINIVASA RAO	Assoc. Prof.	
5	Dr. B. VENKATESWARA RAO	Assoc. Prof.	
6	Dr. J. RAMESH	Assoc. Prof.	
7	Dr. N. VAMSI KRISHNA	Sr.Asst. Prof	
8	Dr. SUBHOJIT DAWN	Sr.Asst. Prof	
9	Sri. P. VENKATESH	Asst. Prof	
10	Dr.S N V S K CHAITANYA	Asst. Prof	
11	Sri.T. SUNEEL	Asst. Prof	
12	Sri. M. L. N. VITAL	Asst. Prof	
13	Sri. R. MADHUSUDHANA RAO	Asst. Prof	
14	Sri. V. HARI VAMSI	Asst. Prof	
15	Dr. A. VEERA REDDY	Asst. Prof	
16	Sri. V. RAVINDRANADH CHOWDARY	Asst. Prof	
17	Dr. K. DHANANJAY RAO	Asst. Prof	
18	Dr. J. VIMALA KUMARI	Asst. Prof	
19	Smt.B.SWARUPA RANI	Asst. Prof	
20	Smt.K.LALITHA	Asst. Prof	
21	Dr.D. INDIRA	Asst. Prof	
22	Smt.V.BINDU	Asst. Prof	
23	Dr.T.NAVEEN KUMAR	Asst. Prof	



SIDDHARTHA

ACADEMY OF HIGHER EDUCATION

DEEMED TO BE UNIVERSITY

(Under Section 3 of UGC Act, 1956)

Kanuru, Vijayawada - 520 007, AP. www.vrsiddhartha.ac.in

91 866 2582333

866 2582334

866 2584930

Minutes of the EEE-BOS meeting of Phase II for the A. Y: 2024-25 held on 24th May 2025 at 10.00A.M through Google Meet.

Dr. P.V.R.L.Narasimham, HOD-EEE department and chairman of Board of studies committee chaired and extended a warm welcome to the BOS members.

Dr. K.Siva Kumar, Professor in Department of Electrical Engineering, IIT Hyderabad, and **Academician** attended as member of BOS committee

Dr. D.Suryanarayana, Professor in Department of Energy systems, IIT Bombay, **Academician** attended as member of BOS Committee.

Dr. T.Muralimohan, Professor in EEE department of JNTU Kakinada, nominated by JNTUK has attended the BOS meeting as **University representative** member.

The **faculty of EEE department**, members of BOS committee attended the meeting.

Dr.P.V.R.L.Narasimham presented the agenda of the meeting to the BOS members.

Agenda:

1. Action taken on the Phase I meeting held on 26-12-2024
2. To approve the changes in the Scheme & Syllabus of the Fifth, Sixth, Seventh & Eighth semesters of VR-23 regulation
3. To approve the CBCS course offered in the fifth semester
4. Any other matter with the permission of the chair

The following points were discussed in the meeting

Item-1: Action taken on the Phase I meeting held on 26-12-2024

Suggestion: Include the latest editions of the textbooks for various courses.

Action Taken: The Latest editions are included wherever possible

In the phase-1 meeting, the Honorable members approved the scheme and proposed syllabus.

Item 2: To approve the changes in scheme of the Fifth semester

Category	Proposed Scheme of 5 th Semester			
	Existing	Credits	After Modification	Credits
Programme Core	2	6	3	9
Programme Elective	1	3	1	3
Open Elective	2	6	1	3
Programme Core Lab	2	3	2	3
Audit Course/Humanities Elective	-	-	1	-
Skill Oriented Course	1	2	1	1
Soft skills	1	1	1	1
EPICS	1	1	1	2
		22 credits		22 credits

H.O.D presented the modifications made in fifth semester scheme compared to existing scheme and presented the syllabus of fifth semester.

It was informed that Power Electronics is shifted from sixth semester to fifth semester as professional core and removed the second open elective –II from scheme.

Resolution: B.O.S members approved the modifications made in scheme of fifth semester.

Item 2: To approve the scheme for the Sixth semester

	Proposed Scheme of 6 th Semester			
	Existing	Credits	After Modification	Credits
Programme Core	3	9	3	9
Programme Elective	2	6	2	6
Open Elective	1	3	1	3
Programme Core Lab	2	3	2	3
Audit Course/Humanities Elective	1	-	1	-
Skill Oriented Course	1	1	-	-
Soft skills	1	1	1	1
Humanities Social Sciences	-	-	1	1
		23 credits		23 credits

H.O.D presented sixth semester scheme and syllabus and stated that no change in scheme with earlier existing one. Industrial drives is offered as second professional core in place of Power Electronics.

Resolution: B.O.S members approved sixth semester scheme.

Item 2: To approve the proposed scheme for the Seventh and eighth semester scheme.

Resolution: B.O.S members approved seventh and eighth semester scheme

B.O.S members **suggested** to motivate the students to write GATE Exam so that they can get selected in PSU jobs with high package.

Item No 3: To Approve CBCS courses offered in the Fifth semester

Course code	Course title	Semester	Regulation
23EE6205	Open Elective-III (MOOCS)	V	VR23
Open elective	Course title	Course duration	Link
Open elective-III	Cloud Computing	12 weeks	https://onlinecourses.nptel.ac.in/noc25_cs11/preview
Open elective-III	Computer Networks and Internet Protocol	12 weeks	https://onlinecourses.nptel.ac.in/noc25_cs15/preview
Open elective-III	Data Analytics with Python	12 weeks	https://onlinecourses.nptel.ac.in/noc25_cs17/preview
Open elective-III	Introduction to Machine Learning	12 weeks	https://onlinecourses.nptel.ac.in/noc25_cs46/preview
Open elective-III	Privacy and Security in Online Social Media	12 weeks	https://onlinecourses.nptel.ac.in/noc25_cs79/preview
Open elective-III	The Joy of Computing using Python	12 weeks	https://onlinecourses.nptel.ac.in/noc25_cs69/preview

The B.O.S members approved the list of CBCS courses offered in fifth semester.

Item 4a: Placement Details

HoD presented placement data for AY:2024-25. Out of 111 students who are eligible for job offers, 108 students got placed in various industries for a total of 132 students. Total number of students placed are 176 out of it total core placements are 124 and software placements are 32. Highest Package in Core is Rs. 7.5L and Highest Package in Software is Rs. 4.05L.

Item No 4b: Faculty Publications

HoD mentioned Departmental research data for the 2024-25, 17 SCI , 9 SCOPUS, 25-IEEE Conferences, three in other confceice and Eight-Book Chapters were published

Finally, the chairman of Board of Studies (BOS) thankall the members for their active participation in the meeting.

Date: 24-05-2025


Dr. P.V.R.L. Narasimham

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