



V R Siddhartha School of Engineering  
Siddhartha Academy of Higher Education  
(An Institution Deemed to be University)  
Department of ECE

Volume 14

Issue 12

July 2026



## NEWSLETTER

### EVENTS ORGANIZED IN THE DEPT

#### GROUND SUPPORT SYSTEMS FOR SATELLITE LAUNCHING

As part of the IETE Student Forum, the Department of Electronics and Communication Engineering organized a seminar on “Ground Support Systems for Satellite Launching” on 29th July 2026 at the ECE Seminar Hall. Dr. B. V. Subbarao, Former Scientist, ISRO and Technical Advisor, IN-SPACe, Department of Space, Government of India, delivered an insightful lecture on Ground Support Systems and their role in satellite launch operations. He discussed key aspects including vehicle integration, testing, telemetry, tracking, communication, and safety systems, along with emerging technologies and opportunities in India’s growing space sector. The interactive session provided students with valuable insights into space technology and motivated them to explore career opportunities in the aerospace and space domains.

VELAMAPUDI RAMAKRISHNA SIDDHARTHA SCHOOL OF ENGINEERING  
**SIDDHARTHA**  
ACADEMY OF HIGHER EDUCATION  
(An Institution Deemed to be University)

in association with

Department of **Electronics & Communication Engineering**  
ECE Distinguished Alumni Lecture Series  
**Seminar on**  
**Ground Support Systems**  
**for Satellite Launching**

Date: 29th July 2026  
Time: 10:30 AM-1:00 PM

Invited Speaker:  
**Dr. BV SUBBARAO**  
Former Scientist, ISRO,  
Technical Advisor, IN-SPACe,  
Dept. of Space, Govt of India  
(1979-1983) Alumnus

Venue: ECE-Seminar Hall  
Targeted Audience: III/IV B.Tech ECE Students

Co-Chairman:  
**Dr. B. Venkata Rao**  
Dean ECE  
SDE-02

Co-Chairman:  
**Dr. S. Suryanarayana**  
Professor, ECE, VSSUT

Co-Chairman:  
**Dr. P. Praveen Kumar**  
Professor, ECE, VSSUT

Faculty Coordinators:  
**Mrs. K. V. Ratna Prabha**  
Assistant Professor, ECE, VSSUT  
Dept. ECE Coordinator  
**Dr. V. Saritha**  
Assistant Professor, ECE, VSSUT  
ECE Coordinator

Student Coordinators:  
**Ch. Mohana** 10-19 B.Tech  
**B. Laksh** 10-19 B.Tech



## OPPORTUNITIES IN THE SEMICONDUCTOR ECOSYSTEM: AN OVERVIEW

ECE Distinguished Alumni Lecture on “Opportunities in the Semiconductor Ecosystem: An Overview” was conducted on 21st July 2026 at the ECE Seminar Hall. The session was delivered by Dr. Murali Krishna Kilaru, Advanced Packaging Technologist, Intel Corporation, Hillsboro, USA (Batch 2001–2005). Dr. Kilaru provided valuable insights into the semiconductor ecosystem, including chip design, fabrication, testing, advanced packaging, and emerging industry trends. He highlighted career opportunities in the semiconductor sector and emphasized the importance of technical skills, continuous learning, internships, and industry-oriented knowledge. Drawing from his experience at Intel, he also shared insights into advanced packaging technologies and the growing semiconductor industry in India. The session concluded with an interactive discussion, helping students gain a better understanding of semiconductor technologies, career opportunities, and future industry trends.

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An Institution of Higher Education, Government of Andhra Pradesh, India

In association with  
**ISTE**

Department of **Electronics & Communication Engineering**  
**ECE Distinguished Alumni Lecture Series on**  
**Opportunities in Semiconductors ECO System Overview**

**Date:** 21<sup>st</sup> July 2026  
**Time:** 11:00 AM

**Invited Speaker:**  
**Dr. MURALI KRISHNA KILARU**  
Advanced Packaging Technologist  
Intel Corporation, Hillsboro, USA  
(2001-2005) Alumnus

**Venue:** ECE-Seminar Hall  
**Targeted Audience:** III/IV B.Tech ECE Students

**COORDINATOR:**  
**Dr. D. Venkata Rao**  
Dean-All Schools  
HOD-ECE

**Dr. A. Jhansi Rani**  
Professor, ECE, VRSSE

**Faculty Coordinators:**  
**Mrs. K. Y. Ratna Prabha**  
Assistant Professor, ECE, VRSSE  
Dept. Alumni Coordinator

**Third Year Class Teachers & Professor Incharge**

**Student Coordinators:**  
**Ch. Mohana**  
III-TY B.Tech

**B. Lokesh**  
III-TY B.Tech



## MASTER'S OPPORTUNITIES THROUGH GATE: FROM SIDDHARTHA TO IISC

Ms. Shanmukha Priya shared her journey from Siddhartha to IISc and provided valuable insights into GATE preparation, goal setting, time management, and strengthening core ECE fundamentals. She discussed effective preparation strategies, study resources, higher education opportunities, and career prospects in research and the public sector. The interactive session motivated students to pursue postgraduate education at premier institutions and encouraged them to plan their careers with confidence and consistency.



## IETE EVENT : CODEXTREME

As part of the IETE Student Forum, the Department of Electronics and Communication Engineering organized “CODEXTREME”, a C and Python Coding Contest, on 24th July 2026 at the ECE Department, V.L. Dutt Block, Siddhartha Academy of Higher Education, Kanuru, Vijayawada. The event aimed to enhance students’ programming skills, logical reasoning, algorithmic problem-solving abilities, and practical coding proficiency. It also encouraged competitive programming and helped bridge the gap between classroom learning and practical coding applications. A total of 90 students registered, with 80 students participating in the contest. 30 students qualified for the second round. The participants were challenged with real-time coding problems focusing on programming logic, debugging, code efficiency, and time management. The event successfully identified promising coding talent, encouraged active participation in competitive programming, and promoted continuous learning and practical skill development among students. Top performers were recognized with cash prizes for their technical excellence.



## TWO-DAY HANDS-ON TRAINING PROGRAMME

The Department of Electronics & Communication Engineering, Siddhartha Academy of Higher Education, successfully organized a two-day national-level hands-on training programme on “Printed Circuit Board (PCB) Design: From Schematic Capture to PCB Layout Methodology” on 18th–19th July 2026 at the DSP Lab and EDC Lab. The programme aimed to bridge the gap between theoretical knowledge and industry practices by providing students with practical exposure to PCB design workflows. The training covered schematic capture, PCB layout, component placement, routing techniques, multi-layer board concepts, and manufacturing preparation using modern design tools.

A total of 80 students actively participated in the programme and demonstrated enthusiastic involvement throughout the hands-on sessions. Under the expert guidance of Mr. Govinda Rao Velamala, participants gained practical experience in electronic design automation (EDA) workflows and real-time PCB design challenges.

The programme enhanced students’ practical skills, troubleshooting abilities, confidence in hardware prototyping, and understanding of industry-oriented PCB design practices. It also contributed significantly to improving their technical competency and employability in electronics, embedded systems, and related domains.

## FACULTY PUBLICATIONS

S. Suryanarayana . et. al Analysis of A First Order Microstrip Bandpass Filter for N78 Sub-6 GHz 5G Applications Using Quarter Wave Transmission Lines, International Journal of Microwave and Optical Technology (Scopus) , July 2026

S. Suryanarayana . et. al Broadband Orbital Angular Momentum Beam Generation Using a Planar Archimedean Spiral Antenna, Microwave and Optical Technology Letters, July 2026

Reddy, M.N., Raut, Pratikhya., Panda, D.K. et al. GaN HEMTs: Device architectures, analytical models, and applications in high power, high frequency, and sensing. Bull Mater Sci 49, 120 (2026). <https://doi.org/10.1007/s12034-026-03639-y>

The poster is for a two-day hands-on training programme organized by the Department of Electronics & Communication Engineering at Siddhartha Academy of Higher Education. The title is "Printed Circuit Board (PCB) Design: From Schematic Capture to PCB Layout Methodology". It includes a photo of the resource person, Govinda Rao Velamala, and lists the convener, co-convenor, coordinators, and venue. The date is 18th-19th July, 2026, and the time is 10:00 AM. The venue is DSP LAB EDC LAB. The poster also features the logo of Siddhartha Academy of Higher Education and mentions its accreditation by the All India Council for Technical Education (AICTE).

Organized by  
Department of **Electronics & Communication Engineering**

Two-Day Hands-on Training Programme on  
**Printed Circuit Board (PCB) Design:  
From Schematic Capture to PCB  
Layout Methodology**

**Date:** 18<sup>th</sup>-19<sup>th</sup> July, 2026  
**Time:** 10:00 AM

**Venue:** DSP LAB EDC LAB

**Convener:**  
**Dr. D. Venkata Rao**  
Dean - All Schools  
HoD, ECE Department

**Co-convenor:**  
**Dr. GL. Madhumathi**  
Professor, ECE

**Coordinators:**  
**Dr. Ch. Raghavendra**  
**Dr. V. Saritha**  
**Dr. M. Bindu Priya**  
**Mrs. B. Alekhya**

**Resource Person:**  
**Govinda Rao Velamala**  
Design Engineer,  
Microlink Pvt. Ltd., Vijayawada

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(Under Section 3 of UGC Act, 1986)  
Recognized by All India Council for Technical Education, New Delhi, India

## FACULTY AS GUEST SPEAKER

Dr. T. Venkata Sainath Gupta, Ph.D. from VNIT Nagpur (SFRF-24), IIT Delhi, Assistant Professor (Selection Grade), Department of Electronics and Communication Engineering (ECE), was the featured speaker at the 2026 Cycle 1 IEEE SPS Seasonal School on “Applied Signal Processing with AI and Cloud.” The programme was organized by the IEEE Signal Processing Society (SPS) in association with the IEEE Hyderabad Section Student Activities Committee, IEEE SPS Hyderabad Chapter, and IEEE SPS Student Branch Chapter of Nawab Shah Alam Khan College of Engineering and Technology, on 25th July 2026, from 9:00 AM to 1:00 PM at Nawab Shah Alam Khan College of Engineering and Technology. Dr. Gupta delivered a technical session on “AutoDL for Biomedical Imaging: From Image Quality Estimation to Deployment – Hands-On.”

The session provided participants with valuable insights into Automated Deep Learning (AutoDL), biomedical image processing, artificial intelligence, and deployment of AI-based solutions, with a practical hands-on orientation. The seasonal school served as an enriching platform for students, researchers, and faculty to gain exposure to emerging developments in Signal Processing, AI, Cloud Computing, and their real-world applications.



**2026 Cycle 1 IEEE SPS Seasonal School on Applied Signal Processing with AI and Cloud**

Friday, 25th July, 2026 | 9:00am to 1:00pm | Nawab Shah Alam Khan College of Engineering and Technology

**Featured Speaker**

**Dr T Venkata Sainath Gupta**  
Ph.D VNIT nagpur, (SFRF-24), IIT Delhi,  
Assistant Professor (Selection Grade),  
V R Siddhartha School of Engineering,  
SIDHARTHA ACADEMY OF HIGHER EDUCATION  
(DEEMED TO BE UNIVERSITY).

**Topic:** AutoDL for Biomedical Imaging: From Image Quality Estimation to Deployment – Hands-On

**Register Now:** <https://forms.gle/HfnCSetuPmdHENQR7>

## FACULTY ACHIEVEMENT

Dr. Venkata Sainath Gupta Thadikemalla received the Best Paper Award for the paper titled "An Enhanced ISTA-Net+ Based Compressive Sensing Framework for SAR Image Reconstruction" in the "SAR Data Processing and Applications" technical session at MIGARS 2026, organized by the Digital University of Kerala, held from 20th to 22nd July 2026.

## FACULTY PARTICIPATION IN CONFERENCES

P. Satyanarayana, et. al., “AI-Assisted Network Security using Hybrid Metaheuristic Optimization Algorithm (AI-HMOA)”, 4th International Conference on Sustainable Computing and Smart Systems · (ICSCSS 2026), 22<sup>nd</sup> to 24<sup>th</sup>, July 2026.

## **PUBLICATION WITH STUDENTS**

### **CONFERENCES**

G. Reshma, P. S. Kumar, S. Divya and K. S. Ramtej, "Cross-Dataset Robust Motor Imagery EEG Classification Using Deep Learning Architectures," 2026 IEEE Nexo-Tech - International Conference on Advanced Technologies and Innovations (ICATI), Palakkad, India, 2026, pp. 1-5, doi: 10.1109/ICATI69399.2026.11604206.

Yarlagadda, Ganesh, Jyothsna Thanniru, Venkata Mohan Sai Chintakrindi, and Venkata Sainath Gupta Thadikemalla. "Automated Quality Estimation of Retinal Fundus Images: A Comprehensive Review of Traditional Features and Hybrid Approaches." In 2026 IEEE Nexo-Tech-International Conference on Advanced Technologies and Innovations (ICATI), pp. 1-6. IEEE, 2026.

Mohammad, Madiya Muskan, Isaac Victor Manda, Sabiha Shaik, Venkata Sainath Gupta Thadikemalla, and Santoshkumar Sabat. "AutoDL Based Model for Quality Assessment of Diabetic Retinopathy Images from DeepDRiD." In 2026 International Conference on Next Generation Electronics (NELEX), pp. 1-6. IEEE, 2026.

### **STUDENT ACHIEVEMENT**

Y. Keerthi Niveditha (228W1A04D0) a B.Tech ECE student (Batch 2022-2026), was awarded "Dr. C. J. Reddy Travel Grant for Undergraduate Student" for 5<sup>th</sup> IEEE Wireless, Antenna, and Microwave Symposium (WAMS 2026) organized by B.V. Raju Institute of Technology, Narsapur, Medak District, Telangana, India during June 10 - 13, 2026. She presented paper titled "Design and Simulation Analysis of Dual-band Implantable Antenna Operating at 1.96 GHz and 4.06 GHz Biomedical Applications" authored by Y. Keerthi Niveditha, V.V.N.S. Koushik, B. Lakshminadh, Moirangthem Santoshkumar Singh, successfully.

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