



NEWSLETTER

EVENTS ORGANIZED IN THE DEPT

ONE-DAY WORKSHOP TITLED "KEYNOTES ON NEXT-GENERATION CHIP DESIGN"

On August 19, 2026, the Department of Electronics & Communication Engineering hosted a one-day workshop titled "Keynotes on Next-Generation Chip Design," led by Mr. Hemanth Chintalapudi, Lead Design Engineer at Cadence Design Systems, Bangalore, under the leadership of HOD Dr. D. Venkata Rao. Designed to bridge the gap between academic theory and VLSI industry standards, the session covered the transition from traditional Karnaugh maps to System-on-Chip (SoC) microarchitecture planning, hardware acceleration of software algorithms, synchronous circuit timing, and live Verilog testbench simulations for multi-bit adders. Alongside practical engineering demonstrations, Mr. Chintalapudi presented a strategic career matrix highlighting core skills in System Verilog, UVM, Static Timing Analysis, and industry protocols, paired with guidance on professional LinkedIn networking and open-source learning ecosystems. Aligned with Sustainable Development Goals 4, 8, and 9, the event significantly enhanced student technical competence, practical verification skills, and overall industry readiness for future roles in RTL design and semiconductor engineering.

SIDDHARTHA ACADEMY OF HIGHER EDUCATION
(Deemed to be University's 3 of UGC Act, 1956)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

ONE-DAY WORKSHOP ON

KEYNOTES ON NEXT-GENERATION CHIP DESIGN

RESOURCE PERSON

Mr. Hemanth Chintalapudi
Lead Design Engineer, Cadence Design Systems India Pvt Ltd, Bangalore

Expertise:
VLSI Design, Digital IC Design, Semiconductor Design, EDA Tools, Industry Practices

DATE: 19/08/2026
TIME: 9.30 AM to 4.30 PM
VENUE: ECE Seminar Hall

ORGANISING COMMITTEE
Dr. M. Bindu Priya
Mrs. B. Alekhya

STUDENT COORDINATOR
Nagabhyru Sri Krishna Kaushik
NIV B.Tech

CONVENOR & HEAD OF THE DEPARTMENT
Dr. D. Venkata Rao

Organized by Department of Electronics & Communication Engineering



TWO-DAY PRACTICAL-ORIENTED WORKSHOP ON “ARDUINO DESIGN CONCEPTS”

The Department of Electronics and Communication Engineering organized a Two-Day Practical-Oriented Workshop on “Arduino Design Concepts” for 2nd-year students on 28th and 29th August 2026 by Class teachers under the guidance of Dr G L Madhumati Professor in charge. Based on the theme “LEARN · INNOVATE · LEAD,” the workshop aimed to bridge theoretical concepts with practical embedded-system applications. A total of 80 students participated in the workshop. The sessions covered embedded system fundamentals, digital computing, microprocessor and microcontroller architecture, ATmega328P, Arduino Uno, sensors, actuators, and hardware interfacing.

The workshop helped students to:

- Develop a strong foundation in embedded systems and microcontrollers.
- Gain practical experience in Arduino programming, circuit interfacing, sensors and actuators.
- Understand the process of converting theoretical concepts into real-world engineering projects.
- Develop interest in IoT, automation and innovative embedded applications.



IEEE SPS SEASONAL SCHOOL ON GREEN & BLUE AGRICULTURE

The IEEE Student Branch (STB64285), Department of Electronics and Communication Engineering, Siddhartha Academy of Higher Education (Deemed to be University), Vijayawada, in association with the IEEE Signal Processing Society (SPS) Hyderabad Chapter, successfully organized the 2026 Cycle 1 IEEE SPS Seasonal School on the theme “Green & Blue Agriculture: Monitoring and Assessment Using Signal Processing” from 10 to 14 August 2026.

The five-day Seasonal School provided an interdisciplinary platform for students, research scholars, faculty members, scientists, and professionals to explore the applications of signal processing and emerging technologies in green and blue agriculture. The programme featured expert lectures and technical sessions covering diverse areas such as remote sensing, Geographic Information Systems (GIS), artificial intelligence, machine learning, image processing, radar technologies, UAV-based sensing, optimization, smart farming, and agricultural monitoring.

Distinguished experts from reputed academic institutions and research organizations shared their knowledge and research experience with the participants through a combination of offline and online sessions. The programme also included an institutional visit to ICAR--Central Institute of Freshwater Aquaculture (ICAR--CIFA), providing participants with valuable exposure to research activities and practical applications in the field of aquaculture and blue agriculture. The Seasonal School witnessed the participation of 95 participants, comprising students, faculty members, research scholars, and professionals from diverse institutions. The programme concluded with a formal valedictory session, marking the successful completion of five days of technical learning, expert interaction, and interdisciplinary collaboration. The organizing team expressed its sincere gratitude to the IEEE Signal Processing Society, IEEE Hyderabad Section, IEEE SPS Hyderabad Chapter, institutional leadership, invited resource persons, participants, and student volunteers for their valuable support and contribution towards making the Seasonal School a successful and memorable academic initiative.





EUREKA! – ROAD TO ENTERPRISE 2026

Eureka! – Road to Enterprise 2026” was successfully organized by the Entrepreneurship Cell (E-Cell), Siddhartha University, on 20th August 2026 at the ECE Seminar Hall, Siddhartha Academy of Higher Education. The programme was conducted with the objective of fostering innovation, creativity, entrepreneurship, and problem-solving skills among students by providing them with a platform to present and pitch their innovative ideas before an expert panel. A significant highlight of the event was the formal inauguration of the Entrepreneurship Cell, marking an important milestone in the University’s efforts to promote entrepreneurship, innovation, and startup development. The event featured student startup pitches, expert evaluation, interactive discussions, and constructive feedback from a distinguished panel comprising Mr. Gandham Kumar NSL Prakash, Co-Founder, Sarvadhara Tech Innovations Pvt. Ltd.; Mr. Harshith Sai Bugatha, IIT Dhanbad alumnus and Founding AI Engineer, Simpo AI; and Mr. Avinash Bhavisetty, Founder and CEO, Chitti Motors. The participating teams presented their ideas, focusing on problem identification, proposed solutions, feasibility, innovation, business potential, and impact. Following the evaluation, EchoOs, represented by Venkata Gopi Nayak, secured First Place; VR TECH, represented by Jwalith Chandram, secured Second Place; and AQUACATCH, represented by Mohan Suraj, secured Third Place. The winners were awarded recognition along with a total cash prize pool of ₹6,000, while consolation prizes and certificates were also presented. As a special recognition, the winning teams were granted direct entry to the Zonal-level round of Eureka! 2026 IIT Bombay – Road to Enterprise, providing them with an opportunity to advance their ideas to a larger platform. The programme concluded with the felicitation of the judges, a vote of thanks, and a group photograph. Overall, the event provided students with valuable practical exposure to ideation, innovation, startup pitching, expert evaluation, and entrepreneurial development, while establishing a strong foundation for future entrepreneurship and innovation initiatives through the newly inaugurated E-Cell.



GUEST LECTURES ORGANISED

The Department of Electronics and Communication Engineering organized a guest lecture on the GATE Examination for third-year students on 24th August 2026 at the ECE Conference Hall. The session was delivered by Mr. Hajarathvali Shaik, Sr. Faculty, ACE Engineering Academy, who highlighted the importance of GATE for higher education, PSU opportunities, and careers in core engineering industries. Students were guided on exam preparation strategies, subject-wise focus, time management, and career opportunities. The interactive session helped clarify students' doubts and motivated them to begin early preparation and set clear academic and career goals.



FACULTY INTERACTION WITH INDUSTRY PERSONAL

Faculty members from the Department of Electronics and Communication Engineering, V R Siddhartha School of Engineering, visited Defence Electronics and Research Laboratory (DLRL), Hyderabad, on 20th August 2026 to gain insights into microwave engineering, signal processing, and antenna technology. The faculty interacted with scientists including Dr. Y. Hemalatha, Dr. Sai Ram, and Dr. Takeshore K and discussed technical challenges, antenna-related problem statements, research opportunities, and possible academic collaboration.

A guest lecture by DLRL scientists was also proposed. The faculty subsequently visited Skyroot Aerospace, where discussions with Dr. Ramakrishna and Dr. Srinivas provided insights into electronic systems used in launch vehicles, including GPS, telemetry, and control modules. Internship opportunities for ECE students were also discussed positively. Overall, the visits provided valuable exposure to defence and aerospace R&D, practical engineering challenges, emerging technologies, and opportunities for collaborative research and student internships



JOURNALS

S.Suryanarayana ...etal published a paper titled "Analysis of a Compact Cross Shaped BPF for Satellite Communication Applications', in JOURNAL OF NANO-AND ELECTRONIC PHYSICS , Vol. 18 No 4, 04019(4pp) (2026) in August 2026.

FDPS ATTENDED BY FACULTY

Dr.Sakhamuri Suryanarayana participated in 6-day Offline ATAL FDP from 24th to 29th August 2026 conducted by Prasad V. Potluri Siddhartha Institute of Technology, Vijayawada

**V R Siddhartha School of Engineering
Siddhartha Academy of Higher Education
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